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	Subclass	ISSUE CLASSIFICATION
	Class	

PATENT NUMBER

U.S. UTILITY Patent Application

KW O.I.P.E. 10/19/01
LNU SCANNED AS Q.A. AA PATENT DATE

APPLICATION NO. 09/997589	CONT/PRIOR D	CLASS 430 257	SUBCLASS 724	ART UNIT 1752 2827	EXAMINER LUAN THAI
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APPLICANTS

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Title

Multi-chip module and method for forming and method for deplating defective capacitors

PTO-2040
12/89

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
				Legal Instruments Examined _____ (Date) _____	

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